6606217 05/16/03

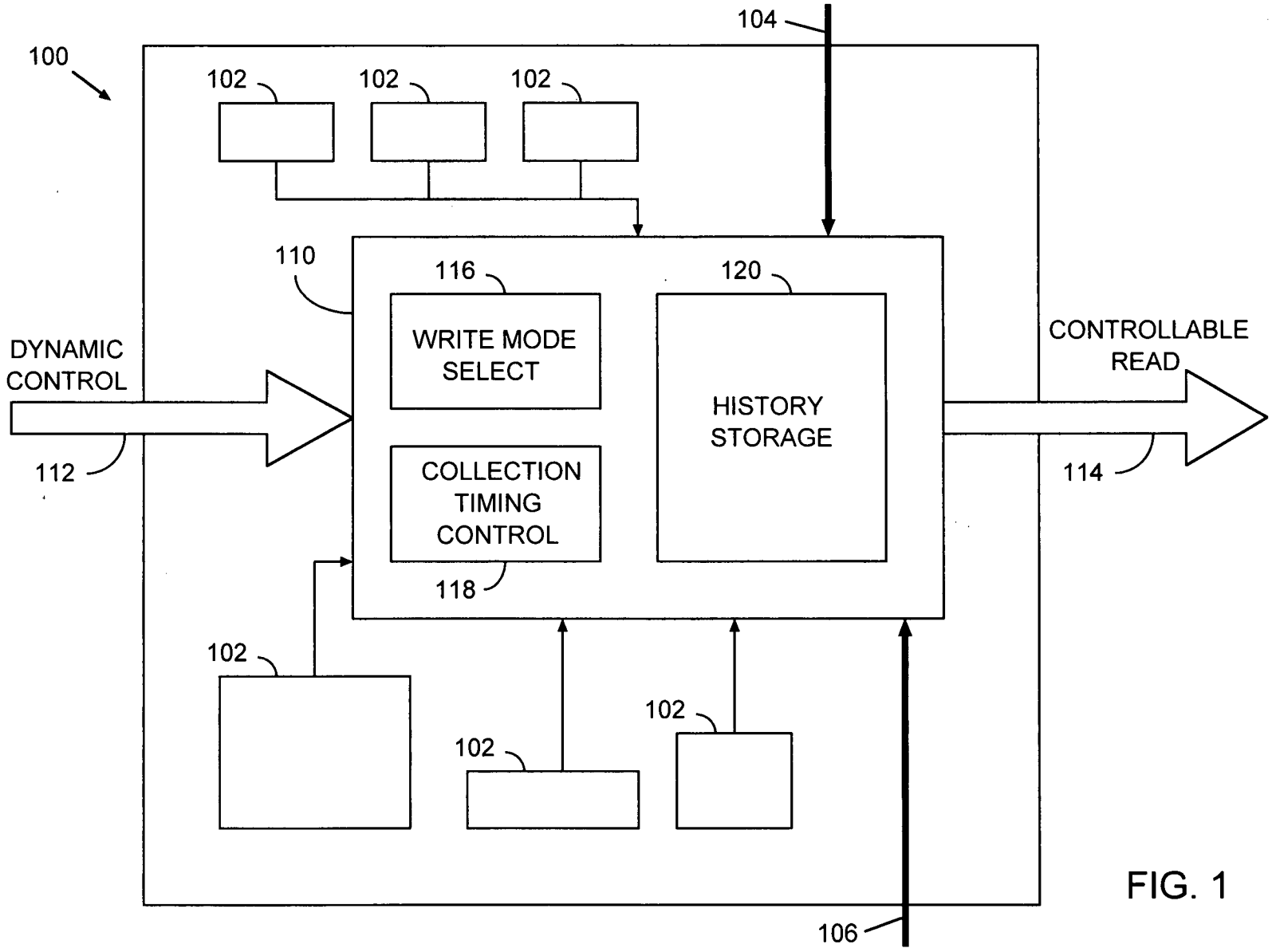

FIG. 1

APPROVED O.G. FIG.
CLASS SUBCLASS
DRAFTSMAN

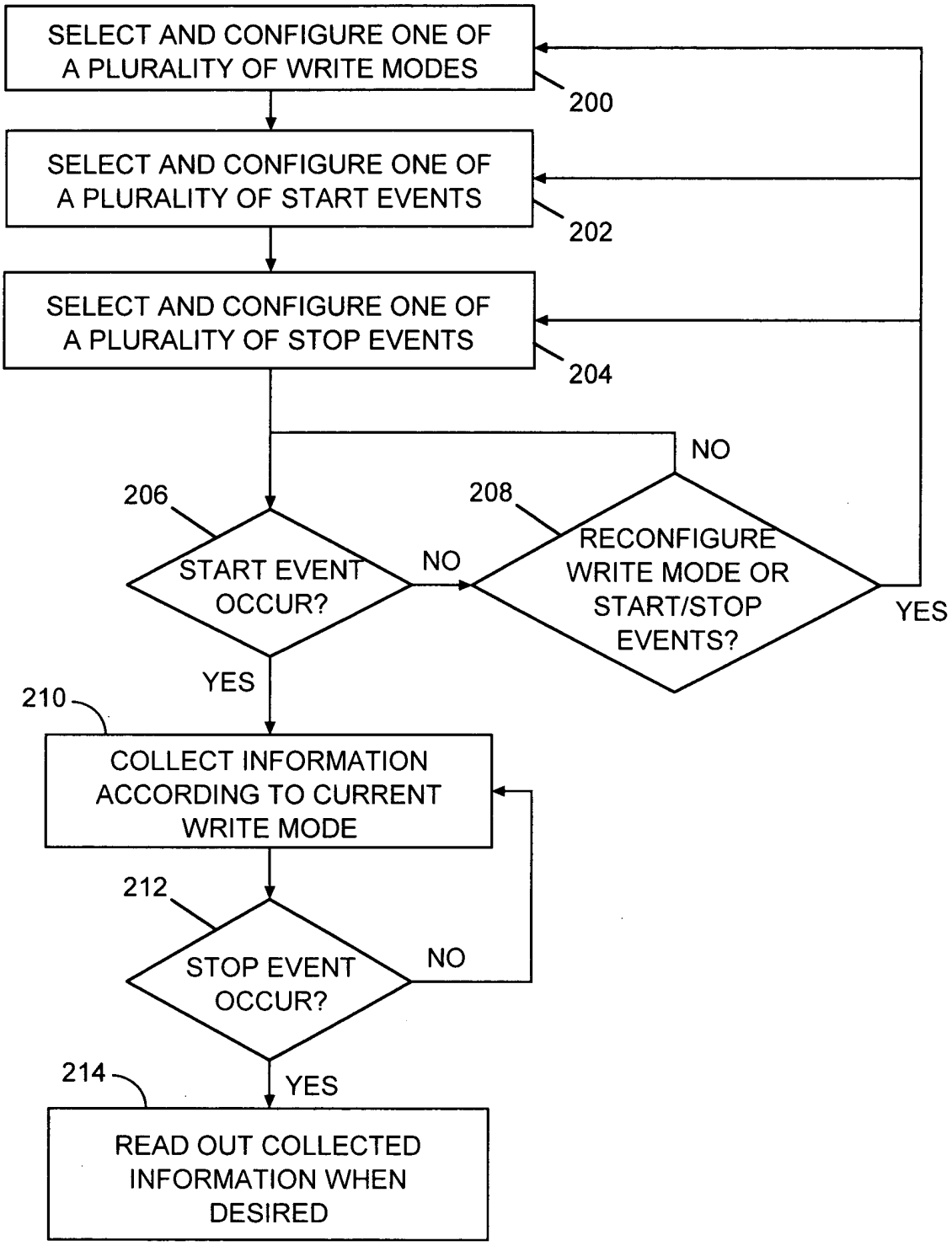

FIG. 2

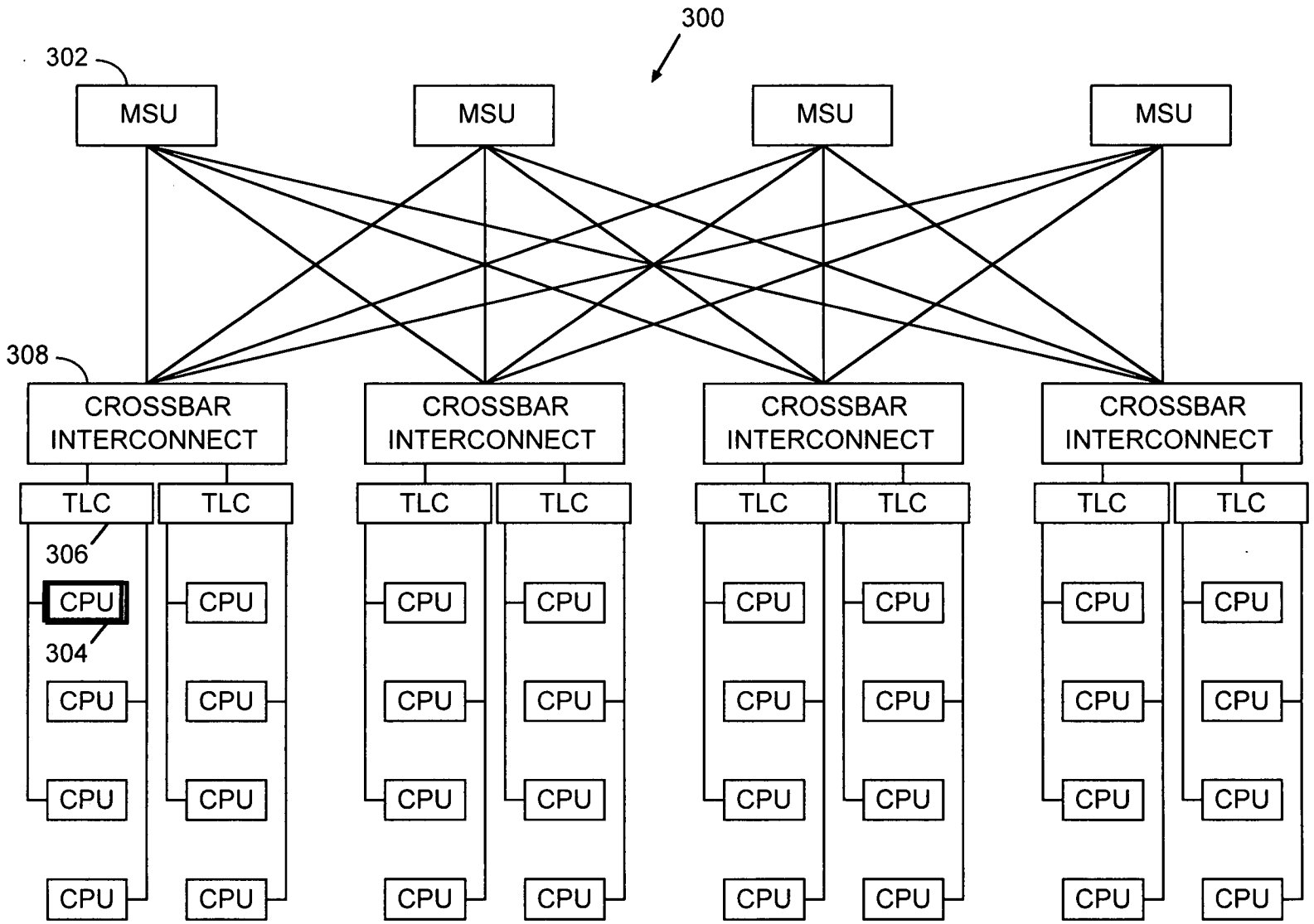
300
302
MSU
MSU
MSU
MSU
308
CROSSBAR INTERCONNECT
CROSSBAR INTERCONNECT
CROSSBAR INTERCONNECT
CROSSBAR INTERCONNECT
TLC
306
CPU
304

FIG. 3

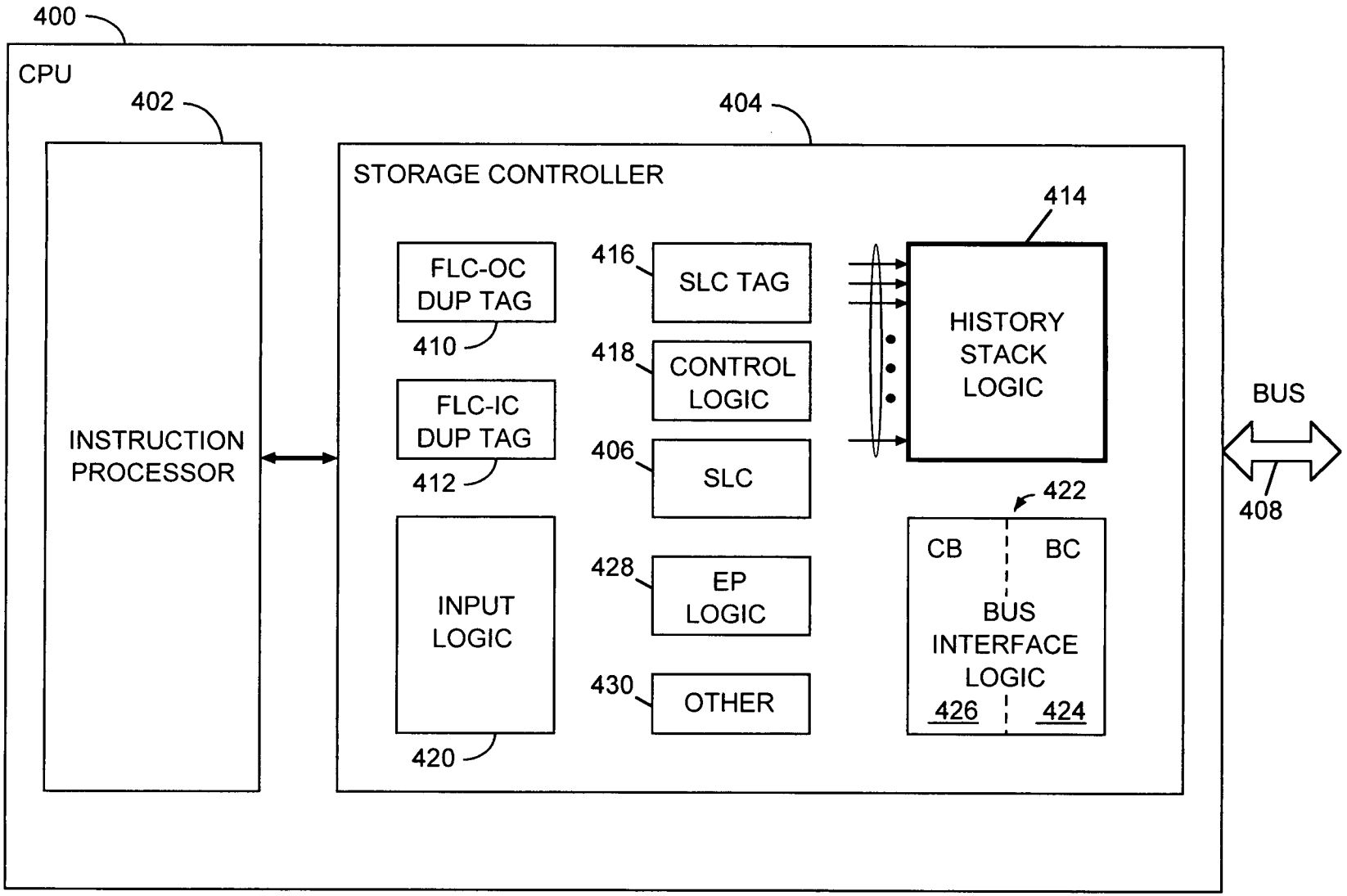
400
CPU
402
INSTRUCTION PROCESSOR
404
STORAGE CONTROLLER
FLC-OC DUP TAG
410
FLC-IC DUP TAG
412
INPUT LOGIC
420
416
SLC TAG
418
CONTROL LOGIC
406
SLC
428
EP LOGIC
430
OTHER
414
HISTORY STACK LOGIC
422
CB
BC
BUS INTERFACE LOGIC
426
424
BUS
408

FIG. 4

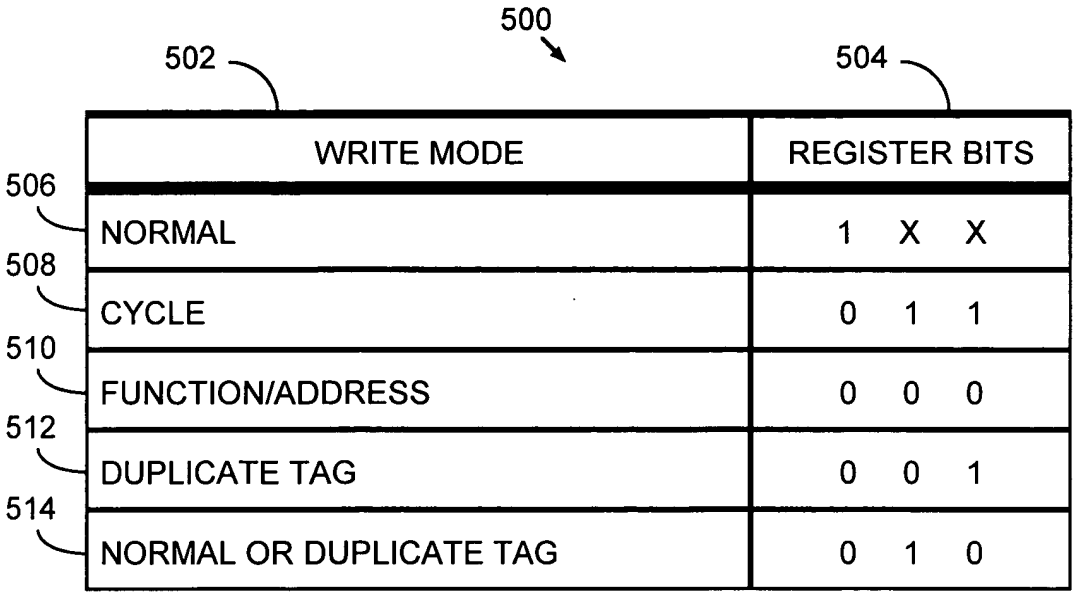
500
502
504
WRITE MODE
REGISTER BITS
506
NORMAL
1 X X
508
CYCLE
0 1 1
510
FUNCTION/ADDRESS
0 0 0
512
DUPLICATE TAG
0 0 1
514
NORMAL OR DUPLICATE TAG
0 1 0

FIG. 5

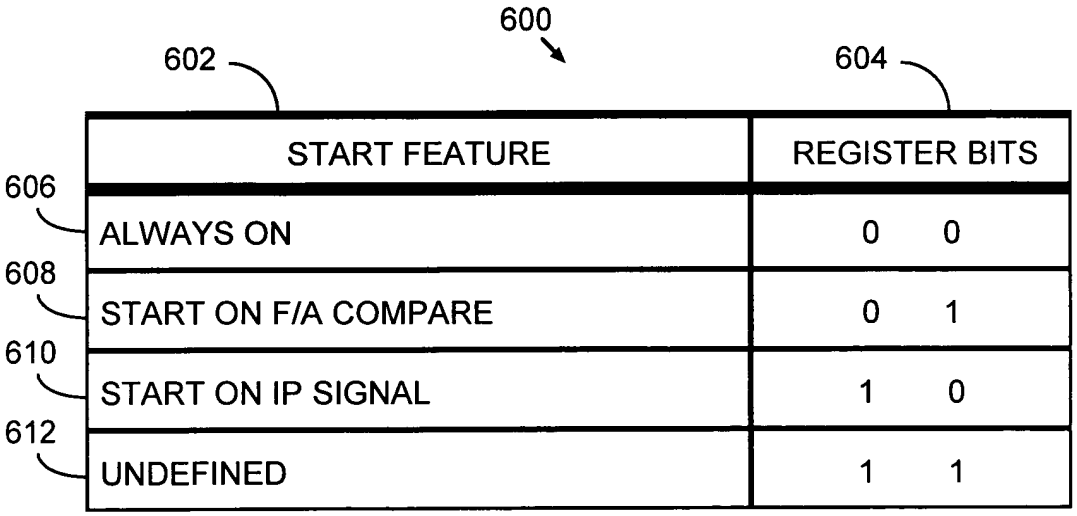
600
602
604
START FEATURE
REGISTER BITS
606
ALWAYS ON
0 0
608
START ON F/A COMPARE
0 1
610
START ON IP SIGNAL
1 0
612
UNDEFINED
1 1

FIG. 6

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BY
TECHNOLOGY SUBGROUP

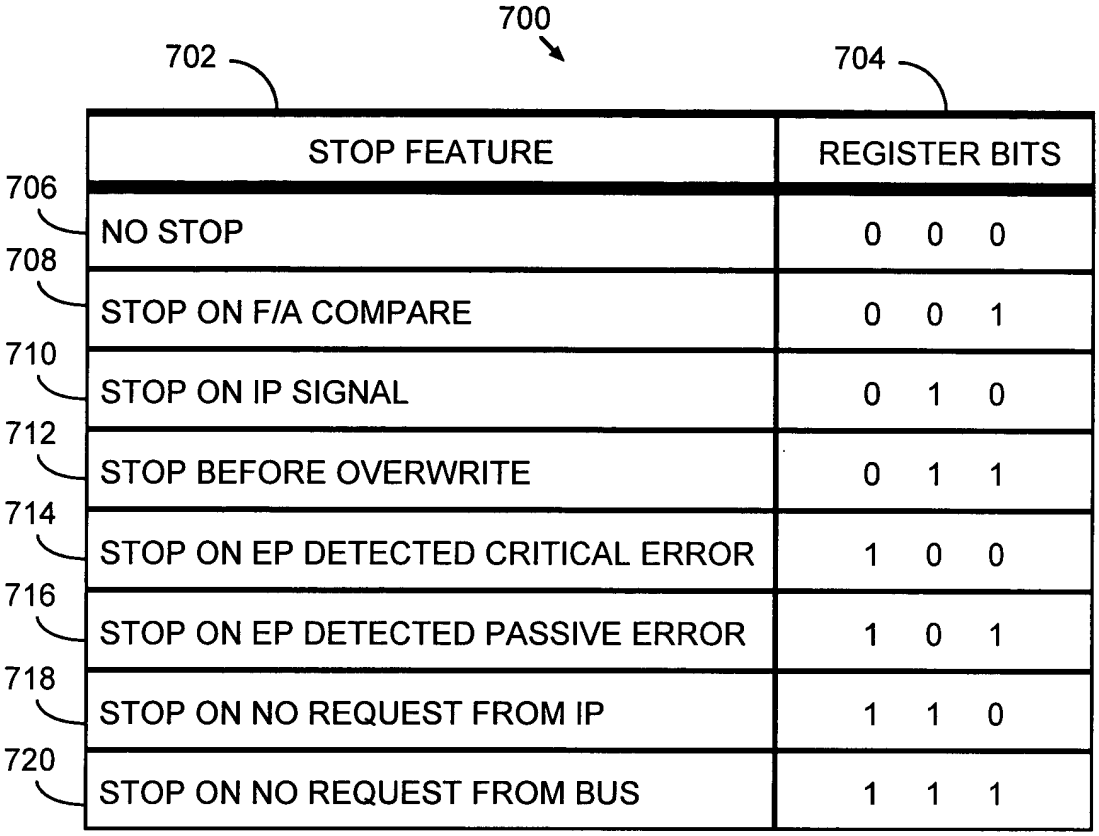

FIG. 7

6065454 EG5/7650

6606E2F E9557h60

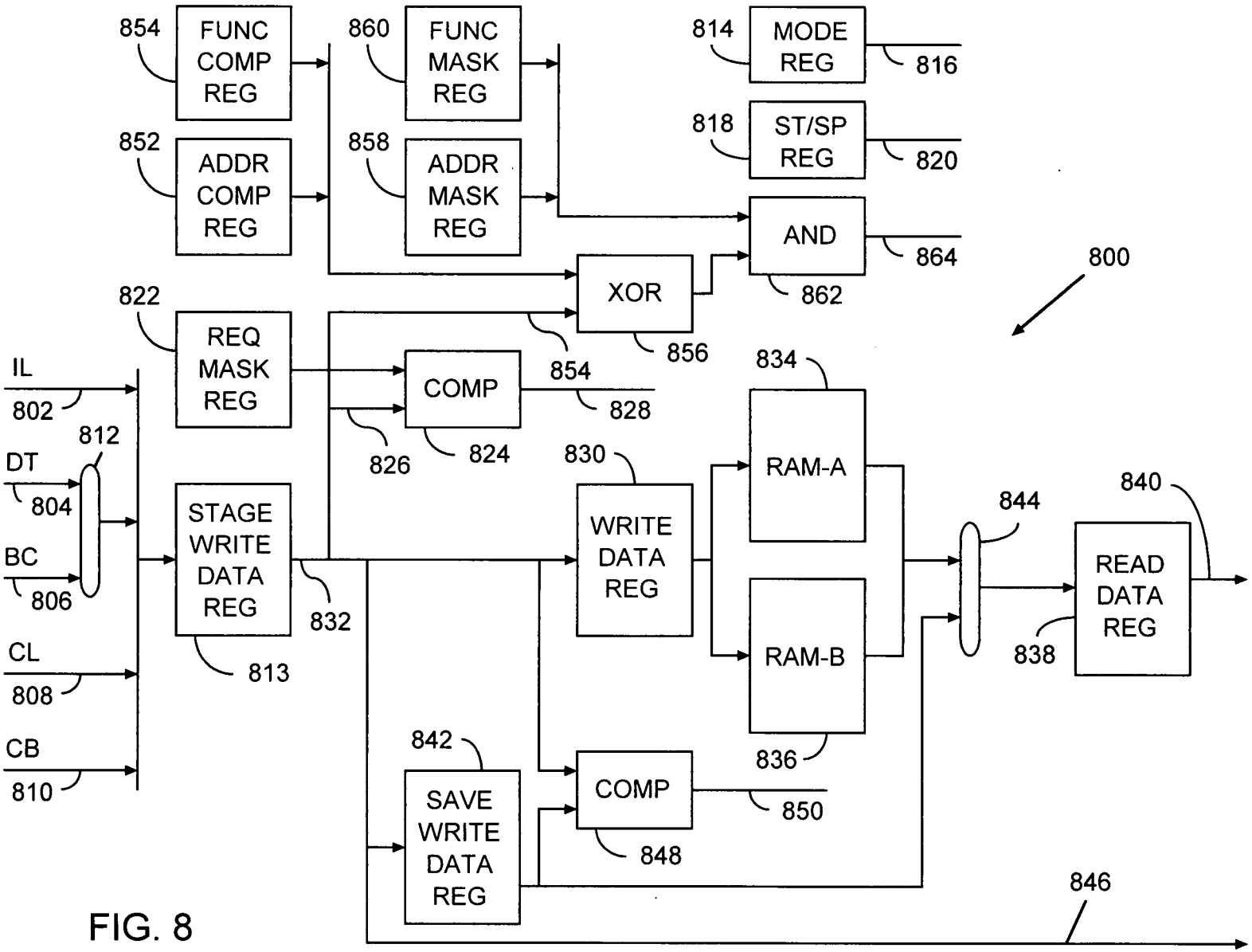

FIG. 8

PATENT 6957460

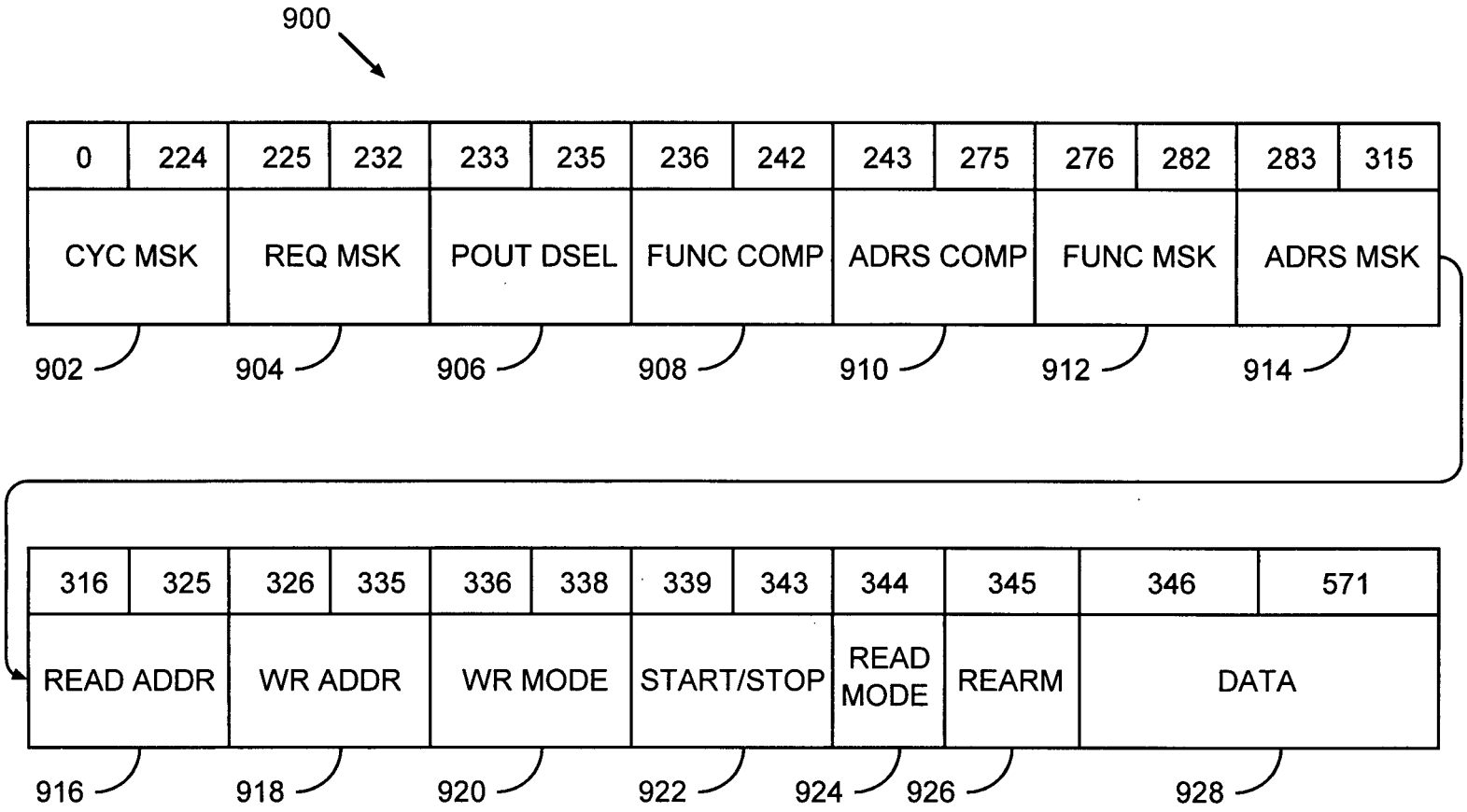

FIG. 9